



2816
JPW

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Confirmation No.: 7046

KOMURA et al.

Group Art Unit: 2816

Application No.: 09/921,561

Examiner: Minh T. Nguyen

Filed: August 6, 2001

Attorney Dkt. No.: 024016-00012

For: DELAY CIRCUIT, SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
CONTAINING A DELAY CIRCUIT AND DELAY METHOD

RESPONSE UNDER 37 C.F.R. §1.111

Mail Stop Amendment

Director for the U.S. PTO

P.O. Box 1450

Alexandria, VA 22313-1450

Date: June 14, 2004

(June 11, 2004 = Holiday)

(June 12, 2004 = Saturday, and)

(June 13, 2004 = Sunday)

Sir:

This paper is filed in response to the Office Action dated March 11, 2004. In the prosecution of the application identified above, please consider the following: